* ABCD parameters for lines, transformers, in general for the transmission systems
* ABCD only works when you have the same amount of ports at input and output
* For MMC it is state-space
* Single grounding for each component, should avoid common grounding
* Delays are not implemented in the MMC
* Double-sided arrow in Jullia equals double equal sign
* There are no ideal DC sources in the power flow. The workaround would be to connected a VSC controlling the DC voltage. For stability there is another way to extract the MMC admittance

For modifying:

using Revise

using HVDCstability

“export” must be used in able

GITLAB:

If you switch branches you should always commit in the changes of the previous branch before